08-6374-DS-ZCH66

JPEG Encoder
Product Data Sheet

v1.1

Updated: April 1, 2010

Features

- Supports Joint Photographic Experts Group (JPEG) image decoding
- Supports 420 or 422 or 444 YUV input
- Supports non-interleaved YUV in separate buffers.
- Supports YUV 422 interleaved input
- Configurability for various quality factors [0,100].
- Configurability to set restart markers into the JPEG bit-stream.
- Supports JFIF and EXIF thumbnails.
- Configurable support for macro-block row/group of macro-block rows based encoding
- Support cropping of input
- Supports raw data output
- Hardware acceleration on i.MX platforms that have VPU

Supported Platforms

- Hardware – i.MX ARM11™ and ARM12 platforms
- Software – eLinux, Windows® Embedded CE operating systems

Performance Details

i.MX ARM11 eLinux Platforms
Typical Specifications:1MP
Performance (MHz): 105MHz
Memory Footprint (KB):
  - ROM: 50.00
  - RAM: 35.75

i.MX ARM11 Windows® CE Platforms
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Memory Footprint (KB):
  - ROM: 50.00
  - RAM: 35.75

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Performance measurements can deviate based on ARM core, memory and cache configuration on the board. To measure directly, enable the TIME_PROFILE in the test application provided in the release package.

For further details, contact Freescale customer representative.