08-6660-DS-ZCH66

G.711 Codec

Product Data Sheet

V 1.1

Date: April 1, 2010

Features

- Fully bit exact with ITU-T G.711
- Supports A-law and µ-law encoding
- Supports A-law and µ-law decoding
- Supports converting from A-law to µ-law
- Supports converting from µ-law to A-law
- Sampling frequency 8 kHz
- 64 kbps compressor output for encoder
- Linear PCM output for decoder
- Variable frame/buffer memory size according to the system needs
- Simple application interface

Supported Platforms

- Hardware – i.MX ARM platforms
- Software – eLinux, Windows® Embedded CE operating systems

Performance Outline

i.MX ARM9™ eLinux

Typical spec: 8KHz, 64kbps for encoder
    8KHz, 128kbps for decoder
Performance (MHz): 0.34 for encoder
    0.28 for decoder
Memory Footprint(KB)
- ROM: 0.25
- RAM: 0

i.MX ARM9™ WinCE

Typical spec: 8KHz, 64kbps for encoder
    8KHz, 128kbps for decoder
Performance (MHz): 0.31 for encoder
    0.25 for decoder
Memory Footprint(KB)
- ROM: 0.25
- RAM: 0

Performance measurements can deviate based on ARM core, memory and cache configuration on the board.
To measure directly, enable the TIME_PROFILE in the test application provided in the release package.

For further details, contact Freescale customer representative.