08-6578-DS-ZCH66

Deinterlace Post Processor

V 1.3

Date: April 1, 2010

Features

- Support ARM9
- Support optimization for ARM1136
- Support optimization for (Cortex-A8)
- Support chroma format 4:2:0, 4:2:2, 4:4:4
- Support multiple deinterlacing algorithms

Supported Platforms

- Hardware – i.MX ARM platforms
- Software – eLinux, Windows® Embedded CE operating systems

Performance Details

i.MX ARM9 eLinux, 400 MHz	**i.MX ARM11 eLinux, 533 MHz**	**i.MX ARM12 eLinux, 800 MHz**
Performance (MHz): 153	Performance (MHz): 73	Performance (MHz): 39.6
Memory Footprint (KB): N/A	Memory Footprint(KB): N/A	Memory Footprint(KB): N/A
Usecase: Stefan, 4:2:0, 720x480@30fps	Usecase: Stefan, 4:2:0, 720x480@30fps	Usecase: Stefan, 4:2:0, 720x480@30fps

Performance measurements can deviate based on ARM core, memory and cache configuration on the board. To measure directly, build and execute the PerformanceTest application provided in the release package.

For further details, contact a Freescale customer representative.